AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A digital camera, comprising:

an imaging device for imaging an object so as to output a first image signal having a first resolution;

a thinning-out circuit for thinning out the first image signal so as to create a second image signal having a second resolution lower than the first resolution;

a memory having at least two memory areas;

a selector for selectively selecting each of said two memory areas;

a writer for writing the second image signal outputted from said thinning-out circuit to one of said two memory areas based on a selection result of said selector;

a reader for reading the second image signal from the other of said two memory areas based on the selection result of said selector; and

a displayer for displaying an image based on the second image signal read out by said reader, wherein said imaging device outputs one screen of the first image signal every first

period, and said reader reading out one screen of the second image signal every second period shorter than the first period,

the first period is an integral multiple of the second period, and

said selector switches a memory area to be selected at an interval of the first period.

Claims 2-5 (canceled)

Claim 6 (previously presented): A digital camera according to claim 1, further comprising:

an instruction key; and

a processor for outputting at predetermined timing a first disable signal, a second disable signal and a third disable signal in response to an operation of said instruction key; wherein

said thinning-out circuit being disabled by the first disable signal simultaneously with the operation of said instruction key,

said reader being disabled by the second disable signal simultaneously with the operation of said instruction key, and

said writer writing the first image signal outputted from said imaging device to said memory and disabled by the third disable signal after one screen of the first image signal has been written.

Claim 7 (previously presented): A digital camera according to claim 6, further comprising a recorder to record the first image signal written on said memory to a recording medium.

Claim 8 (previously presented): A digital camera according to claim 7, wherein said processor cancels the second disable signal from outputting after the first image signal has been recorded, and said reader reading the first image signal from said memory.

Claim 9 (original): A digital camera according to claim 1, wherein said memory has a single signal input/output port.

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